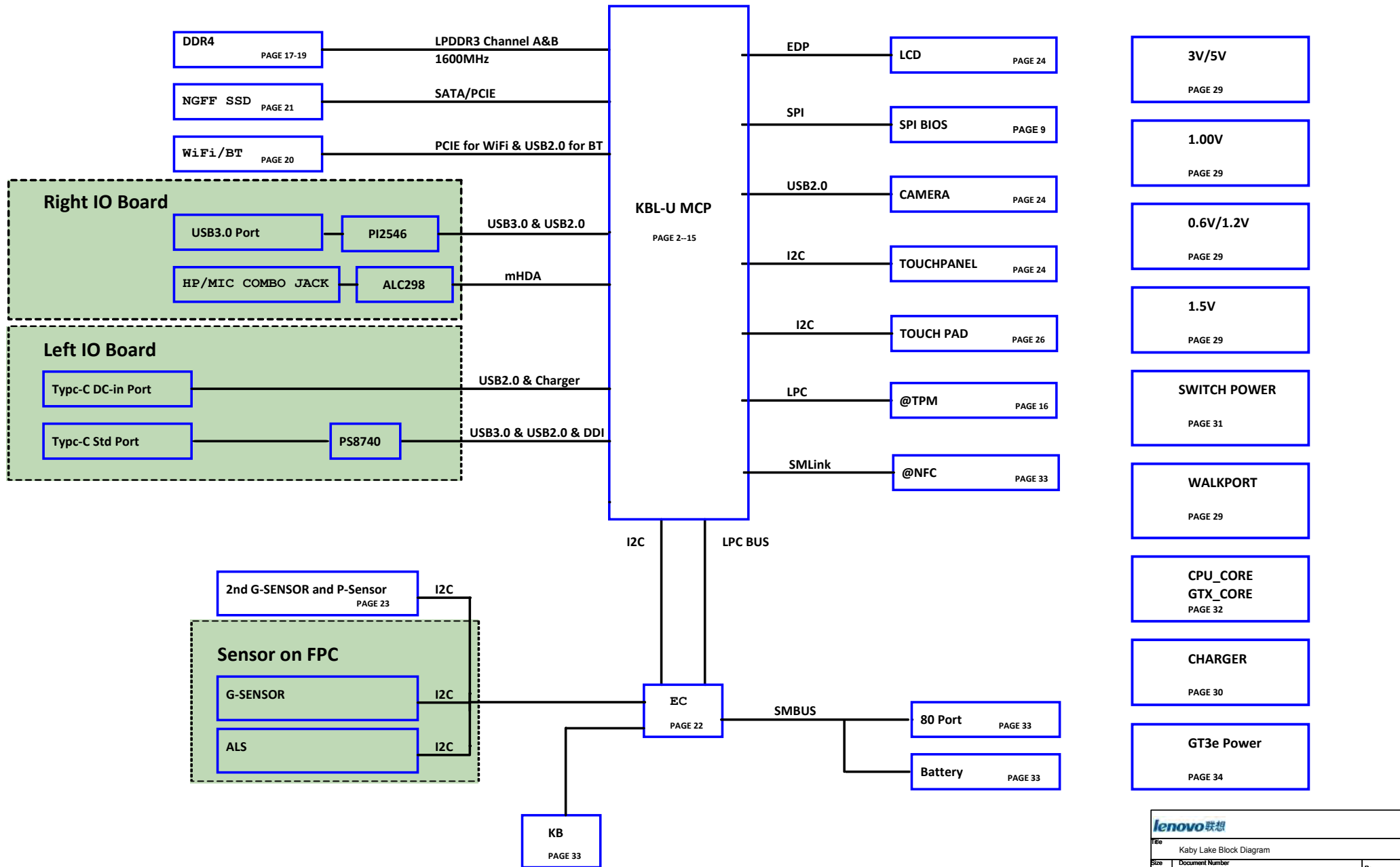
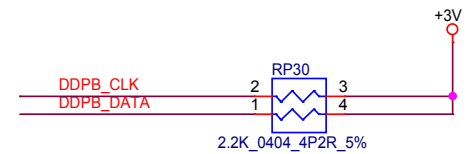
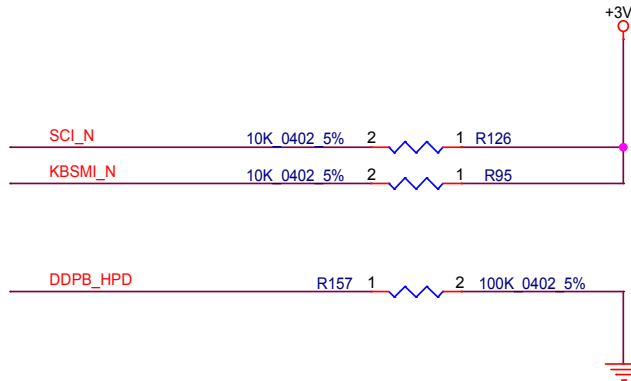
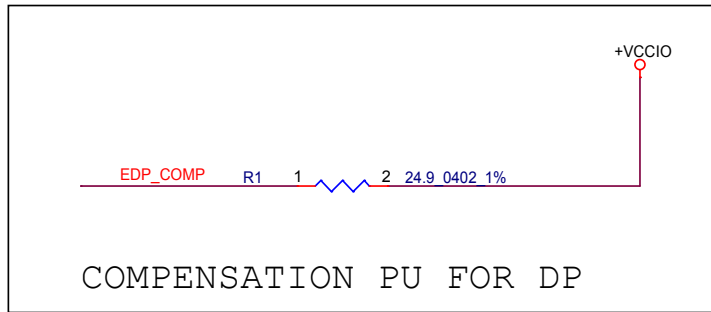
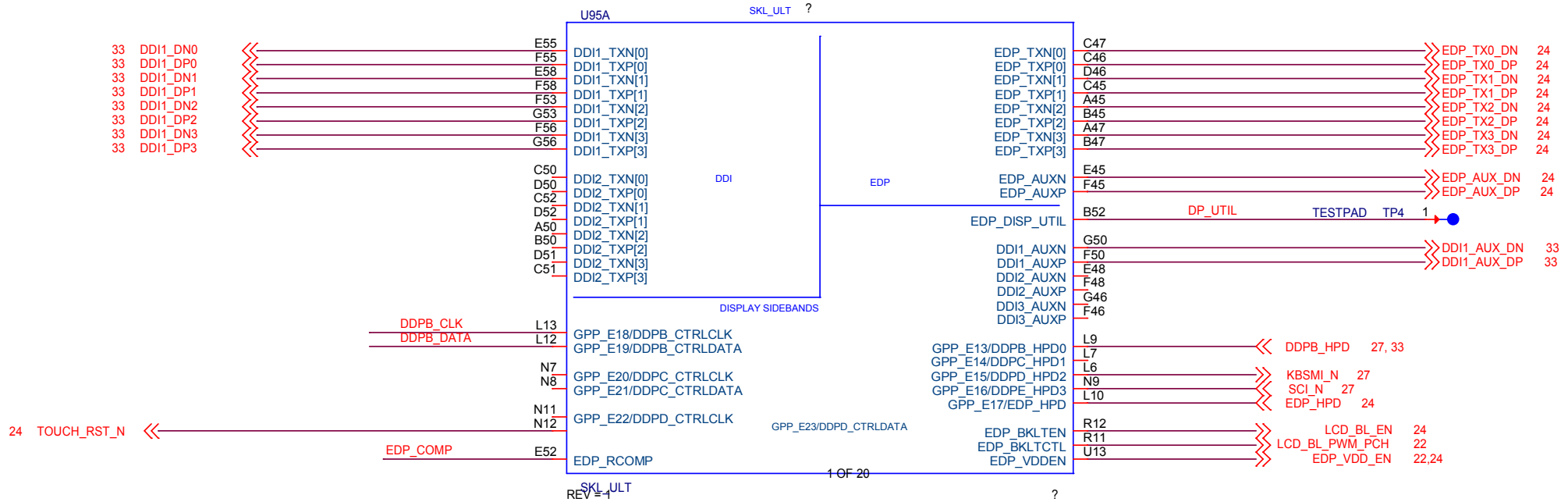
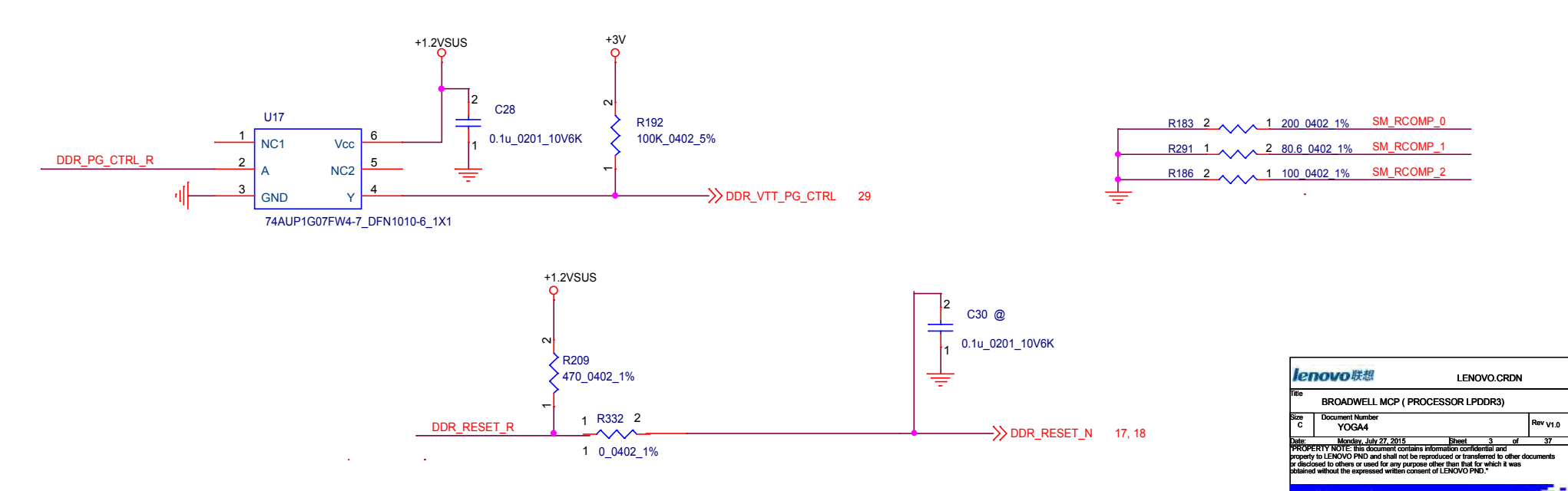
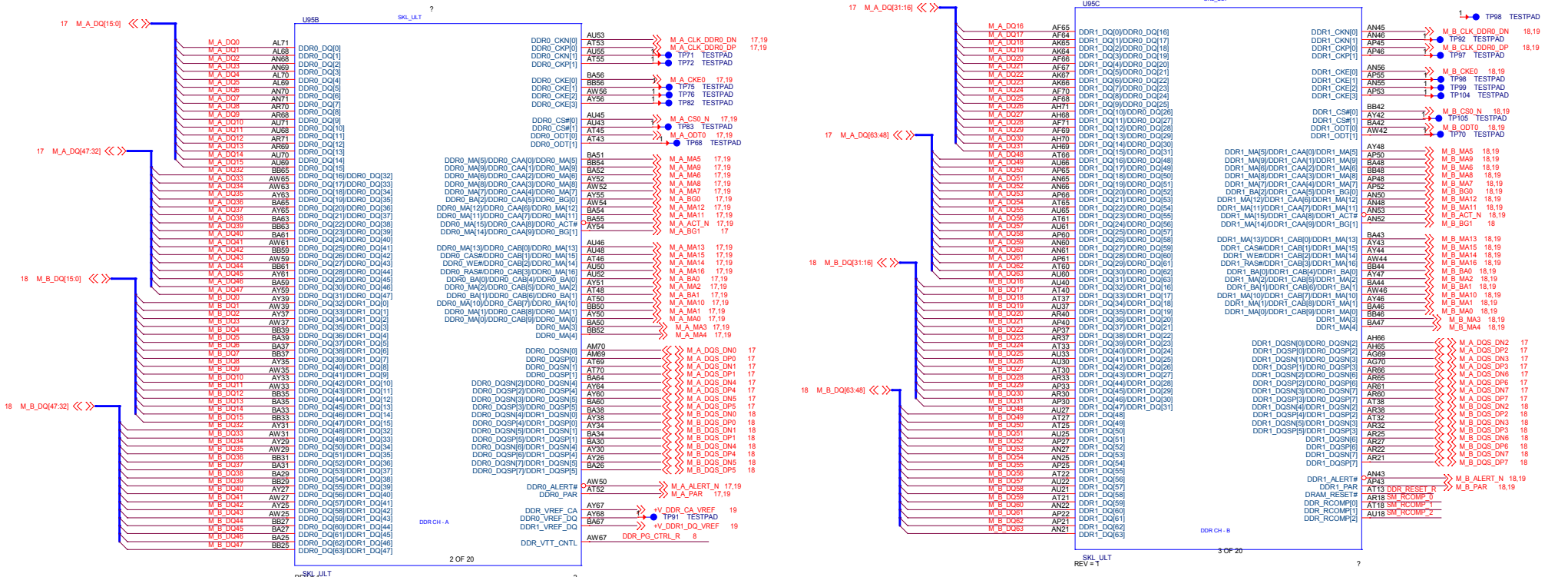


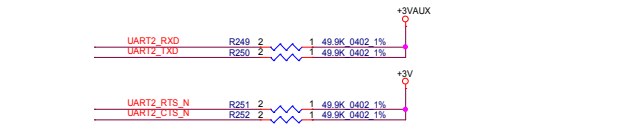
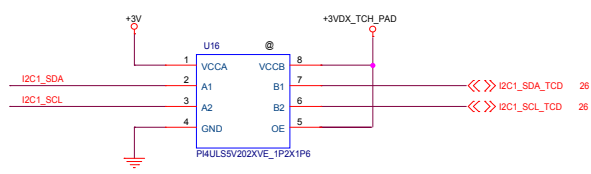
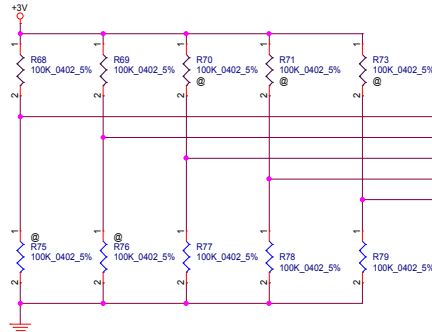
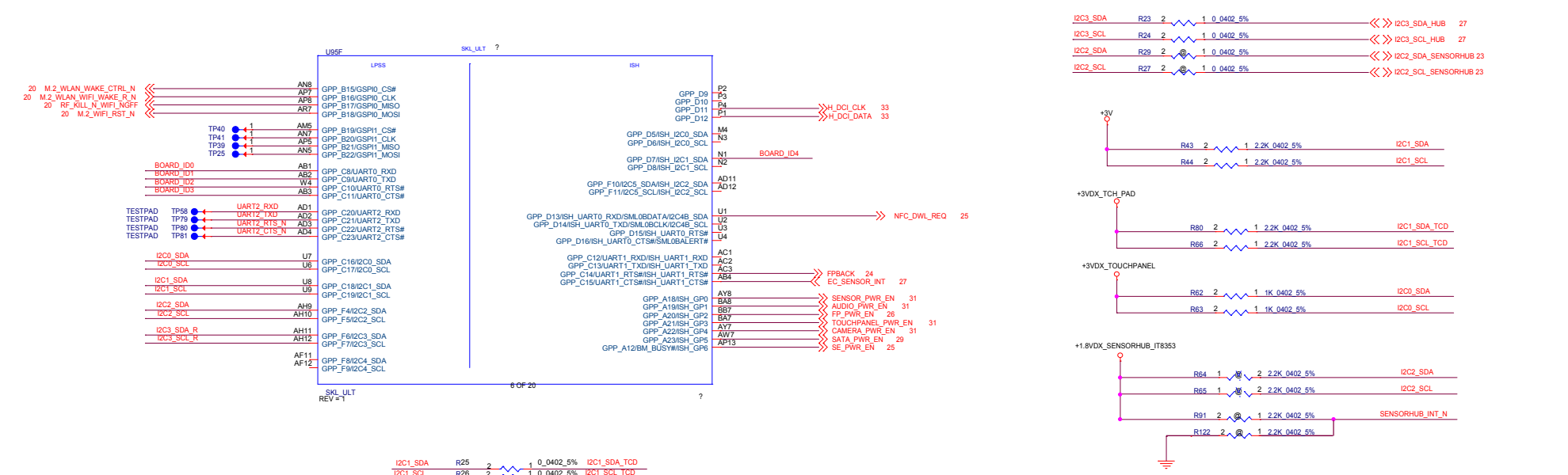
**Agera-SVT**  
**CYG50 NM-A901**  
**Schematic Block Diagram**








Vinafix

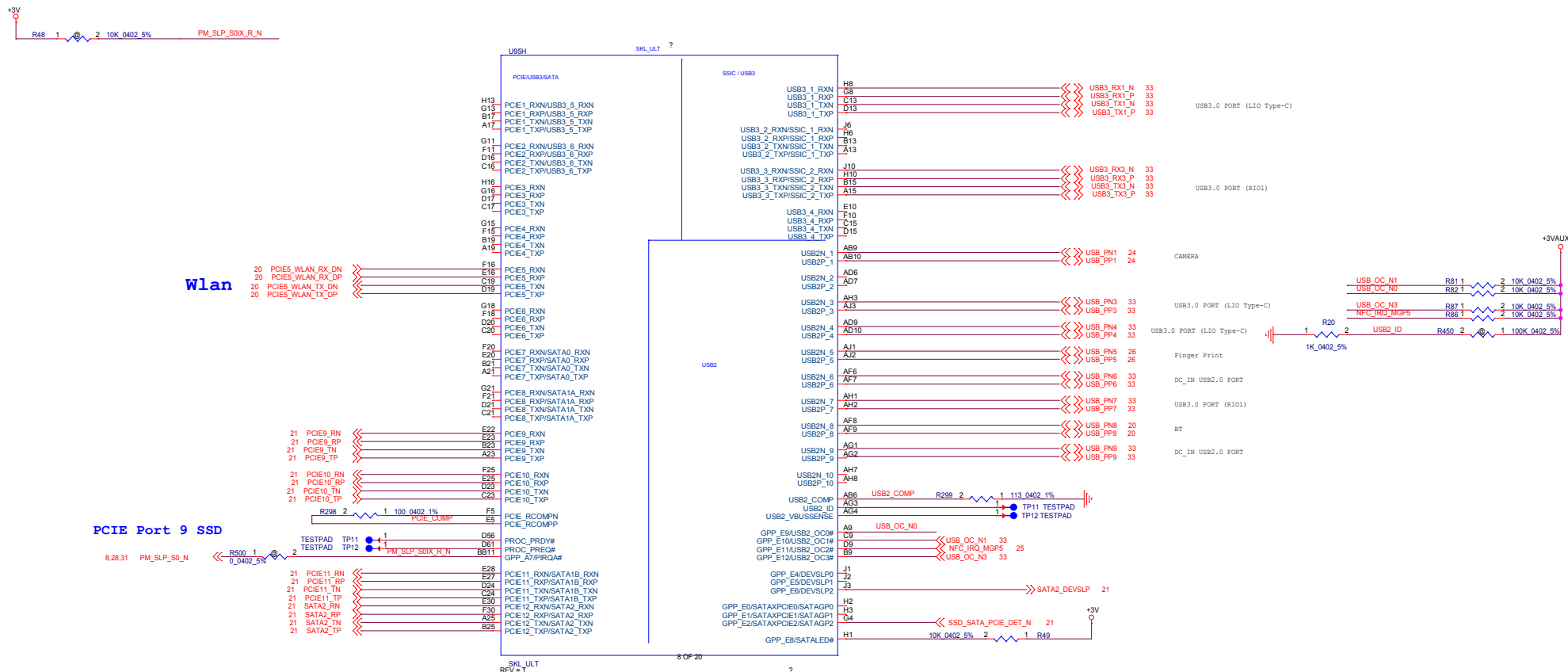


R163, R153	R580, R581, R146, R154	R183	R571, R572, R574, R575, R576, R577, R578, R579	BOARD_ID4	BOARD_ID3	BOARD_ID2	BOARD_ID1	BOARD_ID0	Description
Stuff	Dummy @	200ohm	0ohm	0	0	0	0	0	Samsung 8Gb x16 DDR4 2133
Stuff	Dummy @	200ohm	0ohm	0	0	0	0	1	Samsung 4Gb x16 DDR4 2133
Dummy @	Stuff	121ohm	240ohm	0	0	0	1	0	Micron 16Gb x16 DDR4 2133
Stuff	Dummy @	200ohm	0ohm	0	0	0	1	1	Micron 8Gb x16 DDR4 2133
Stuff	Dummy @	200ohm	0ohm	0	0	1	0	0	Samsung 8Gb x16 DDR4 2400
Stuff	Dummy @	200ohm	0ohm	0	0	1	0	1	Samsung 4Gb x16 DDR4 2400
Dummy @	Stuff	121ohm	240ohm	0	0	1	1	0	Micron 16Gb x16 DDR4 2400
Stuff	Dummy @	200ohm	0ohm	0	0	1	1	1	Micron 8Gb x16 DDR4 2400
Dummy @	Stuff	121ohm	240ohm	0	1	0	0	0	Samsung 16Gb x16 DDR4 2400

联想

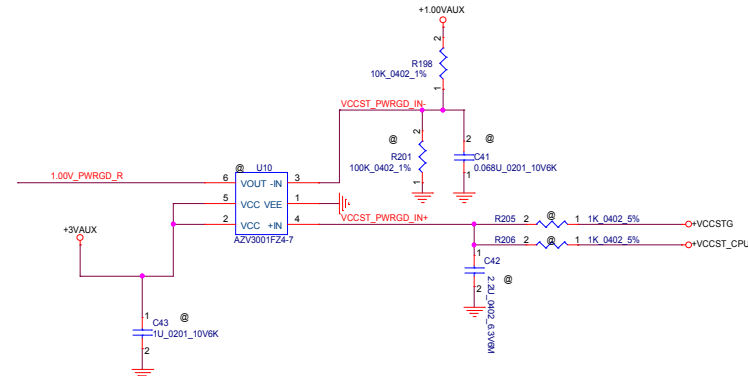
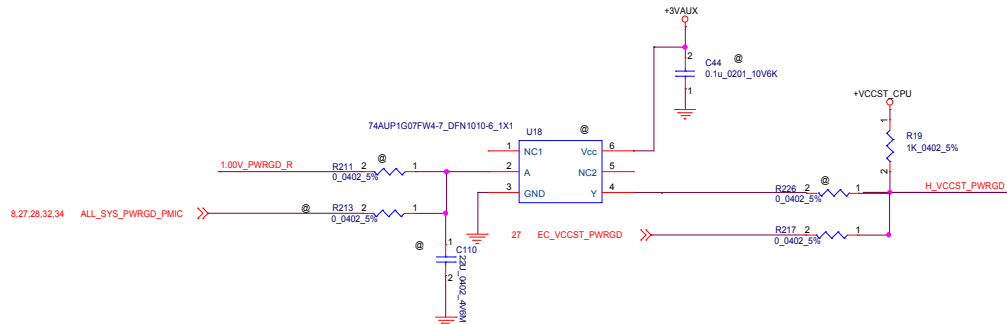
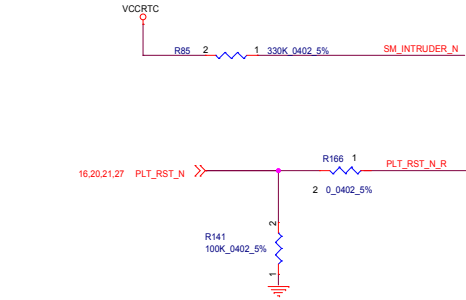
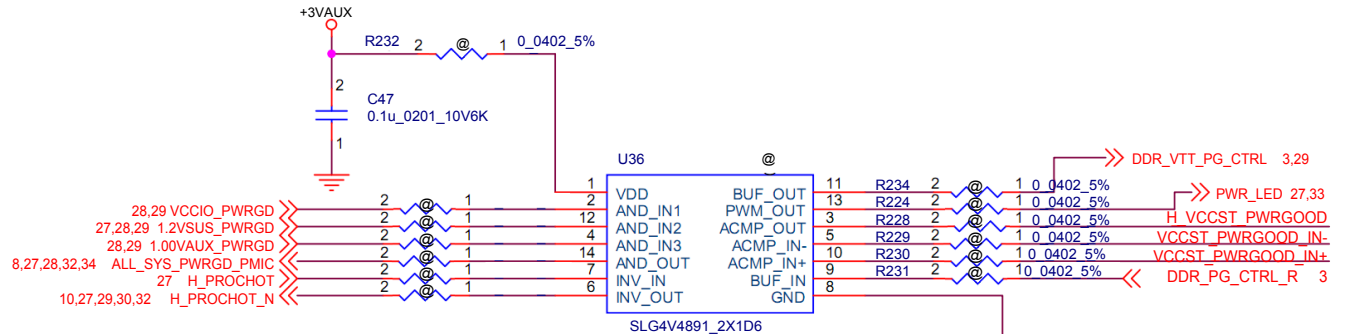
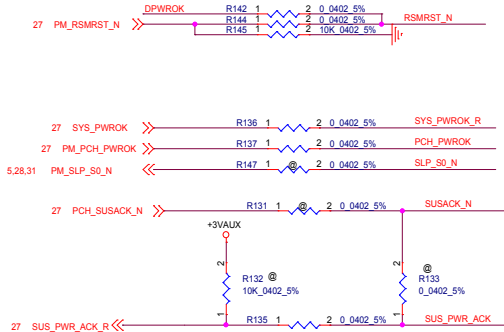
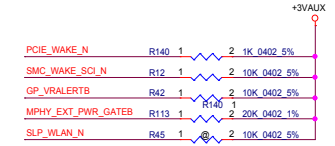
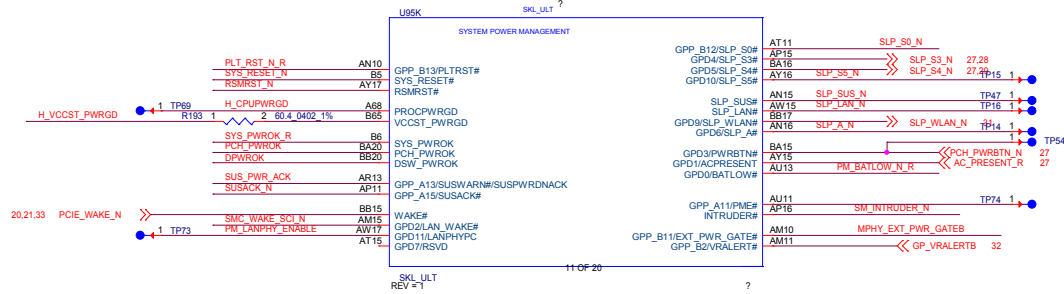
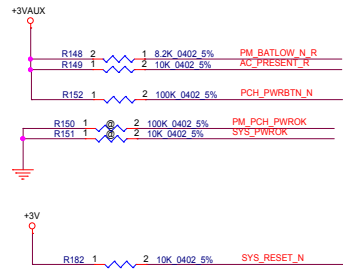
LENOVO.CRDN

File		
BROADWELL MCP (PCIe USB GPIO)		
Size	Document Number	Rev
C	YOGA4	v1.0
Date: Monday, July 27, 2015		
Sheet 4 of 37		
PROPERTY NOTE: This document contains information confidential and property to LENOVO PND and shall not be reproduced or transferred to other documents or disclosed to others or used for any purpose other than that for which it was obtained without the expressed written consent of LENOVO PND.		

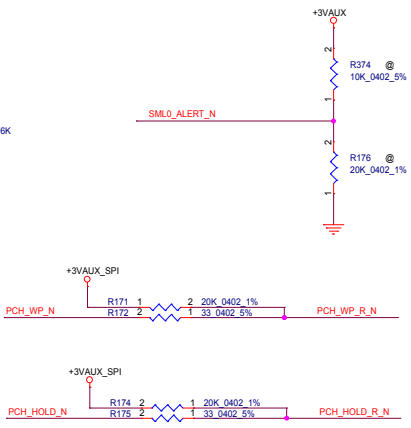
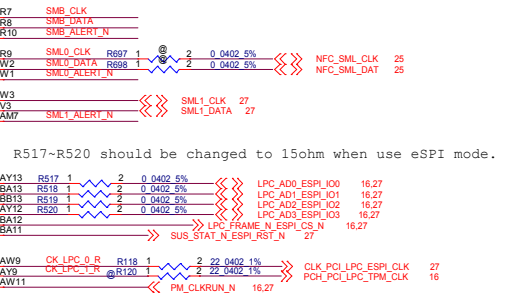
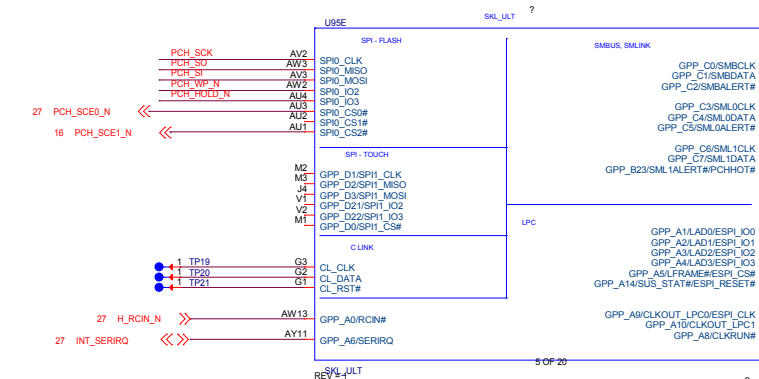
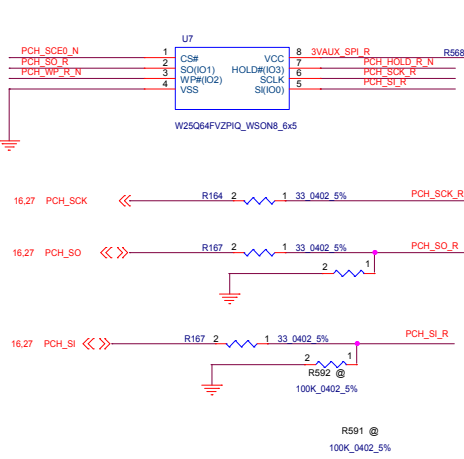
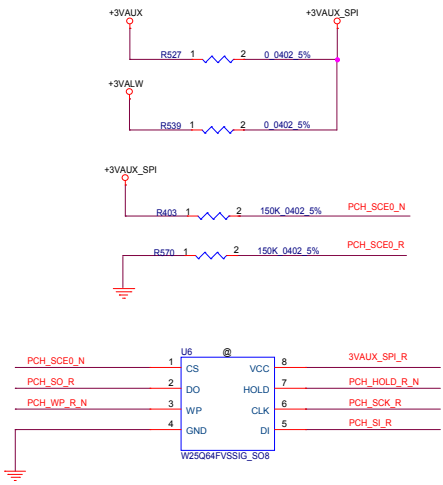




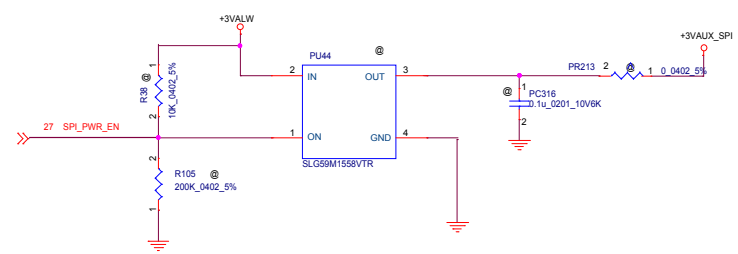
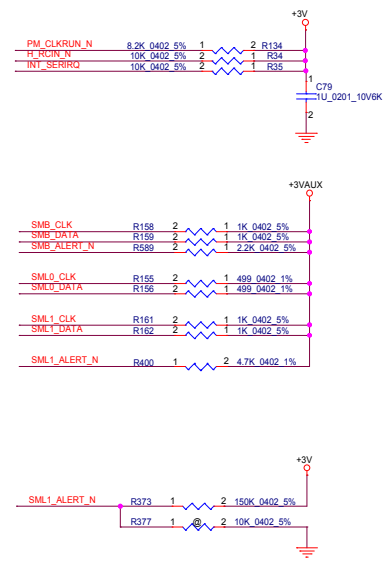


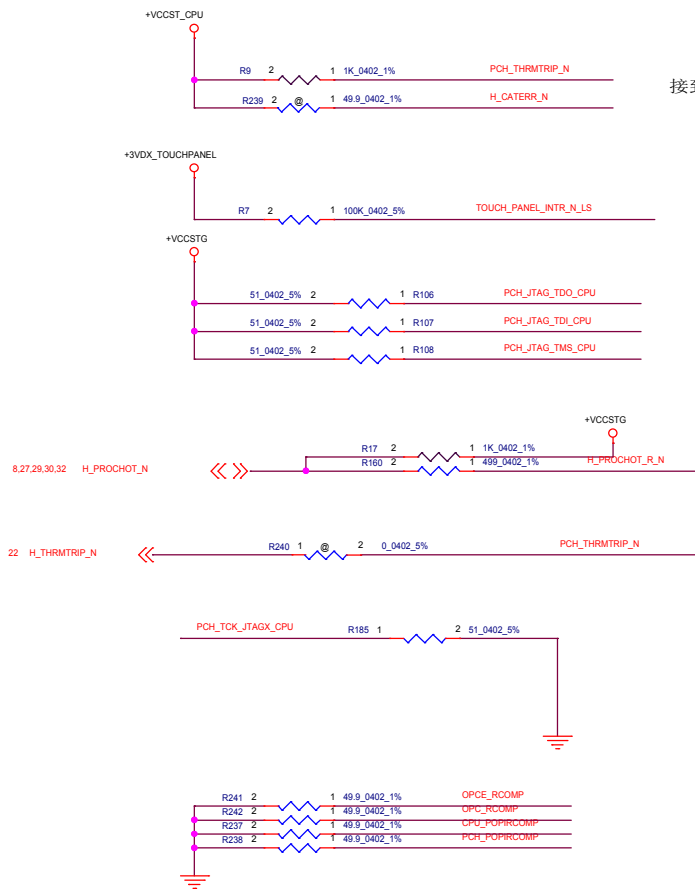




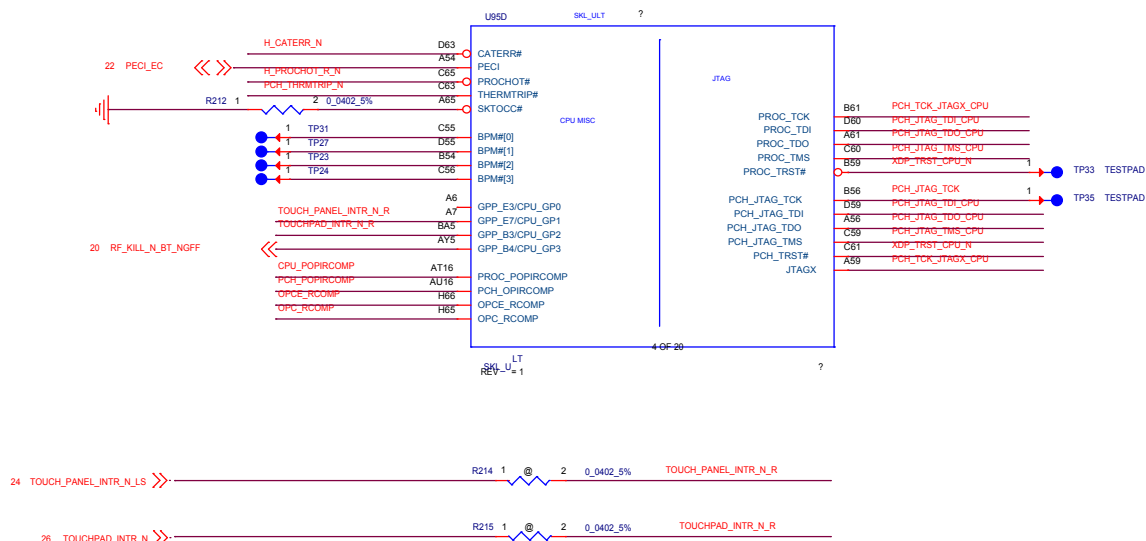


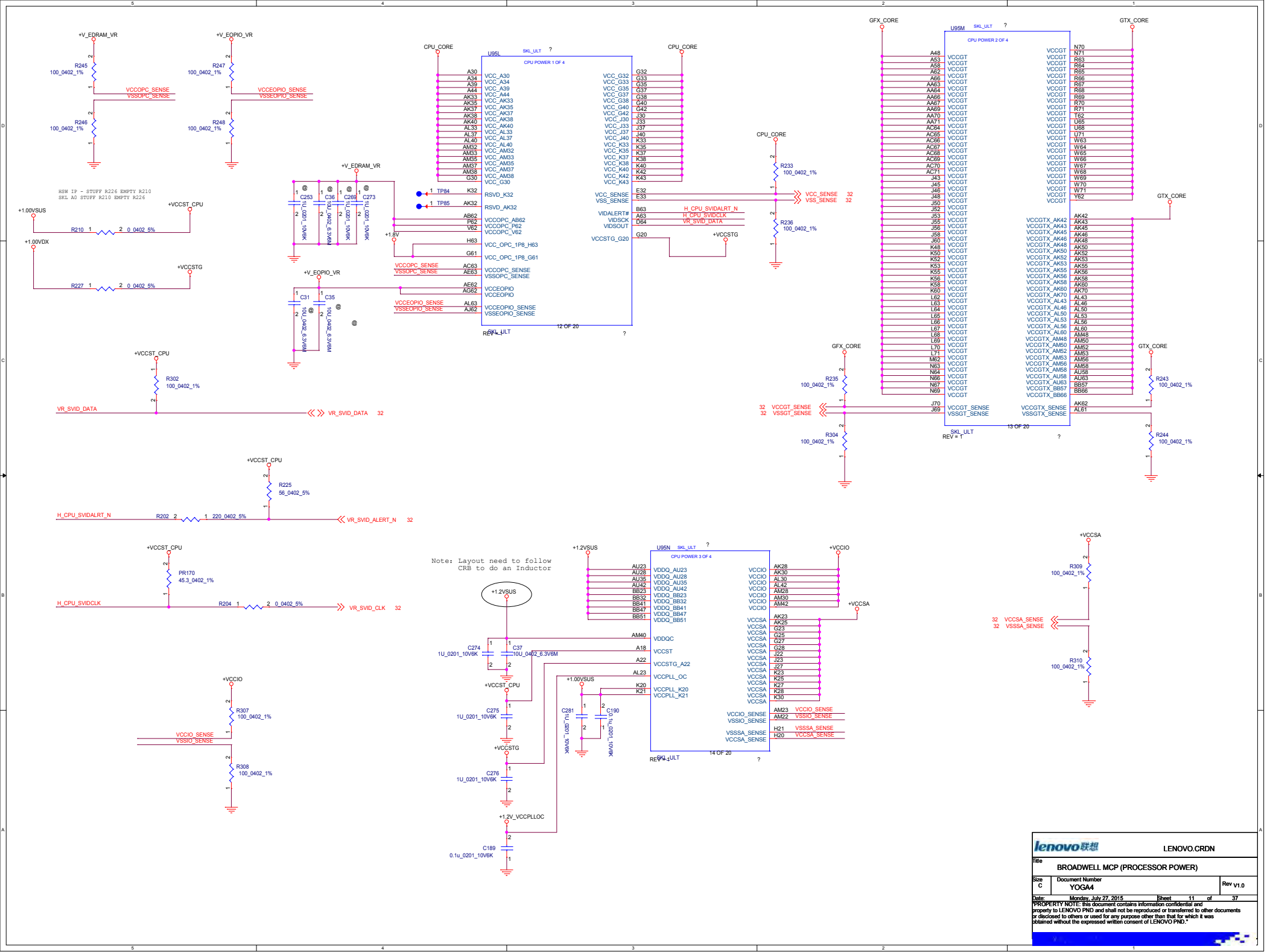
ESPI OR LPC  
HIGH: ESPI IS SELECTED FOR EC  
LOW: LPC SELECTED  
WEAK INTERNAL PD



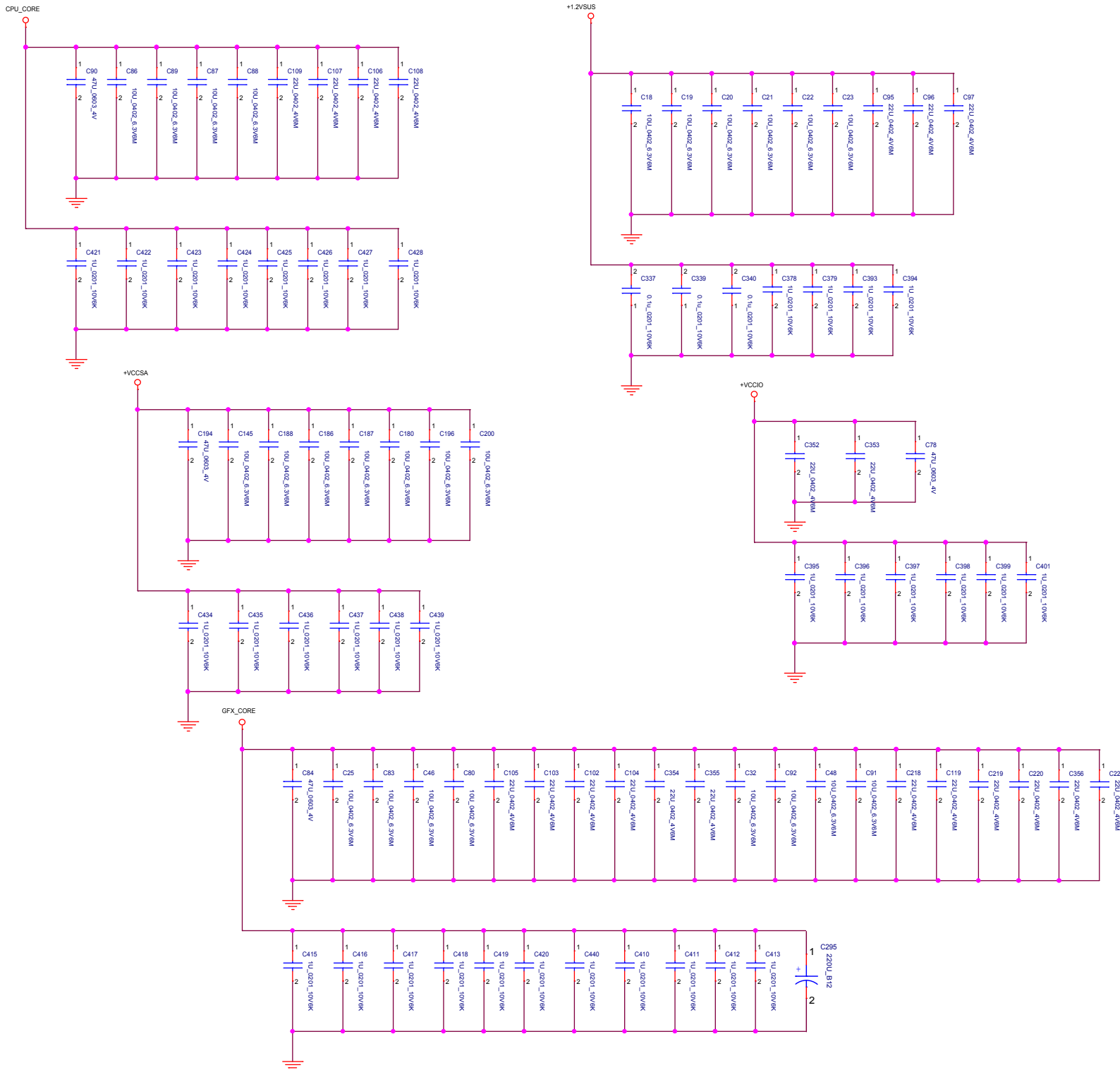


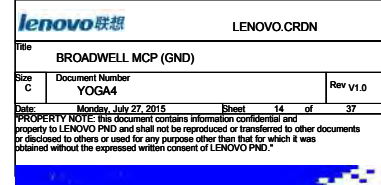
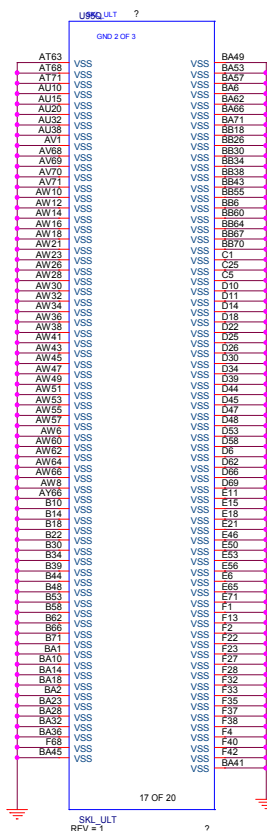
接到EC 1.00V GPIO

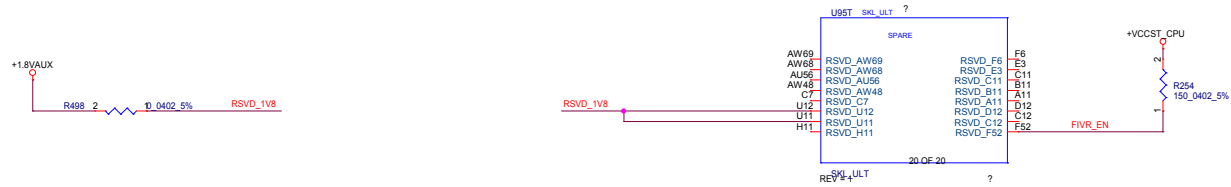


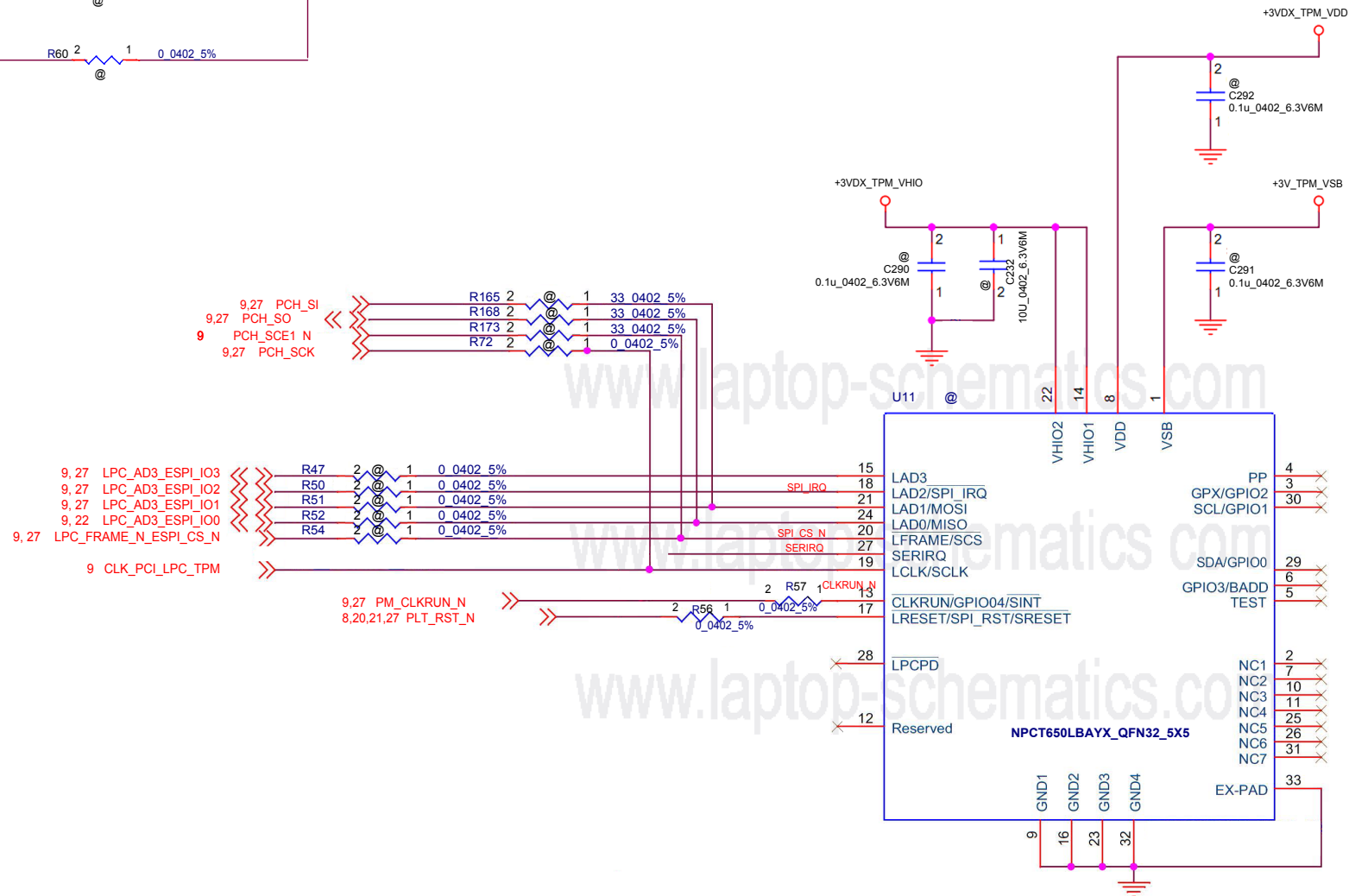
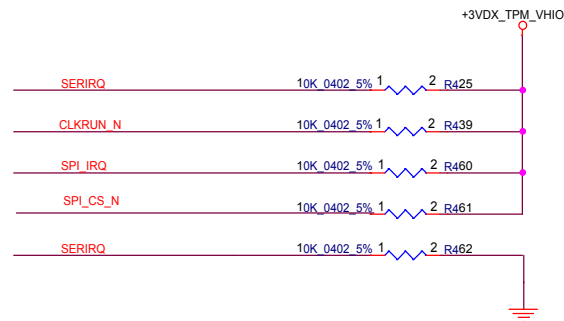
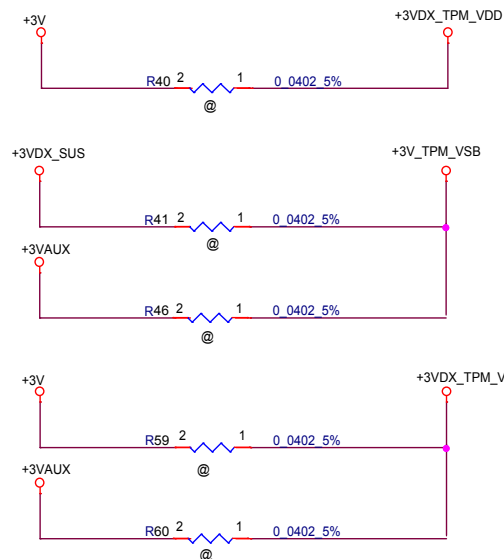




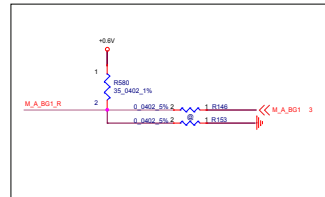




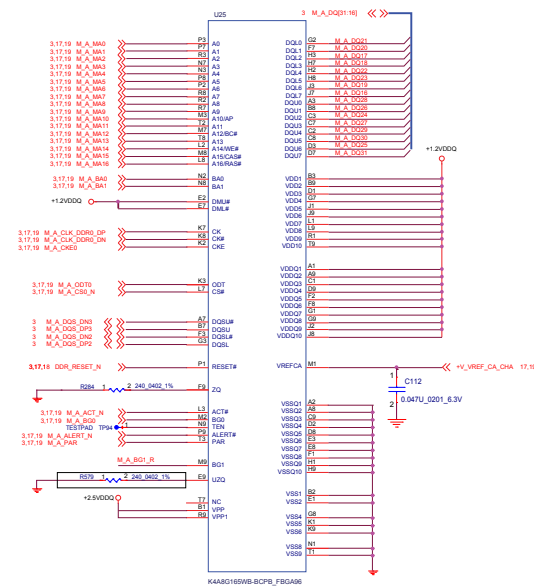


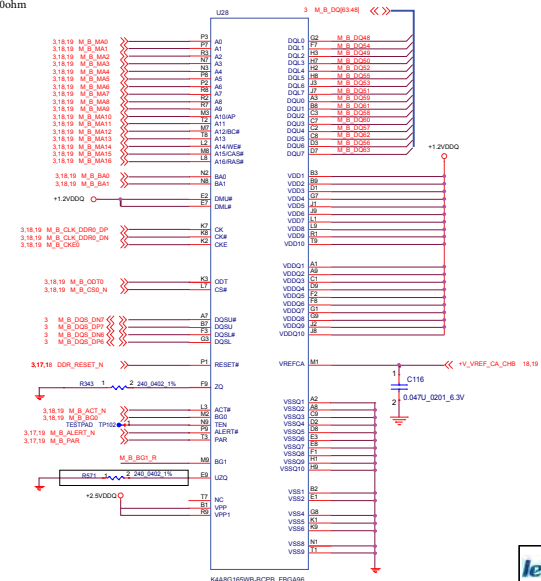
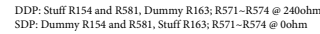
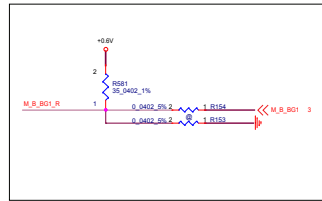


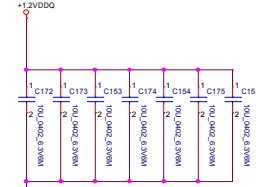
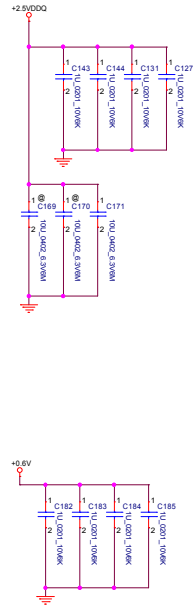
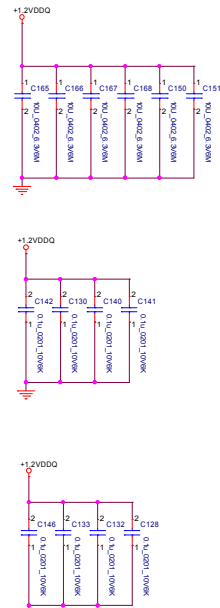
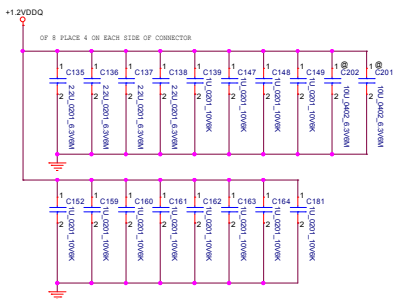
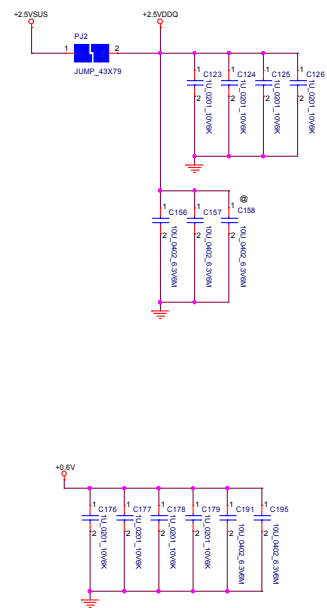
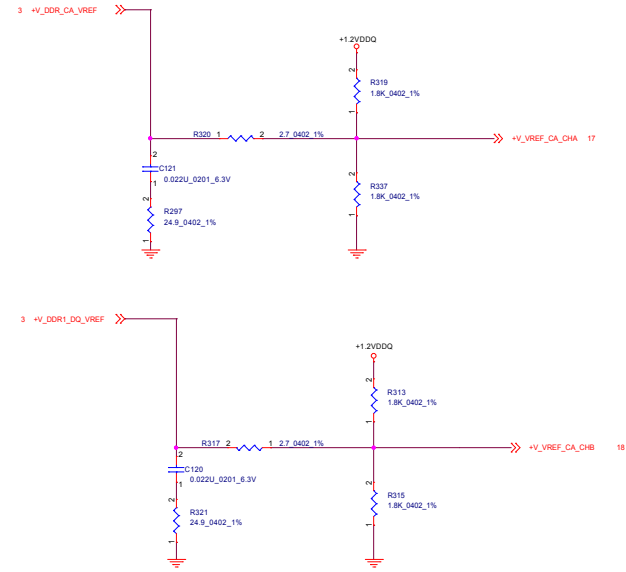
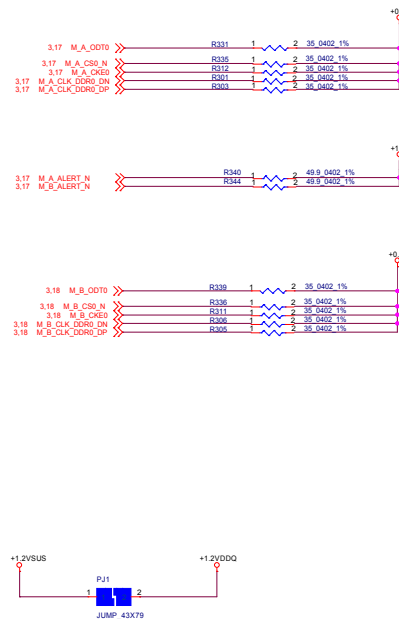
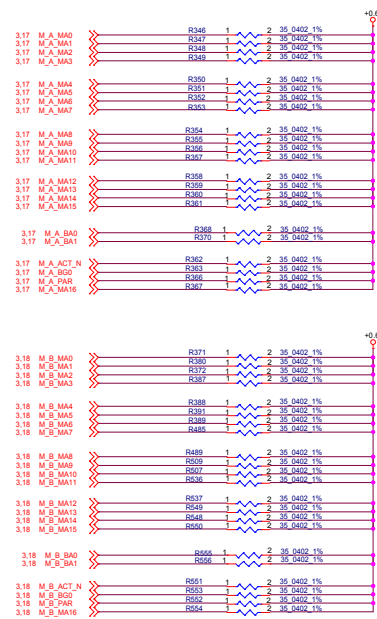




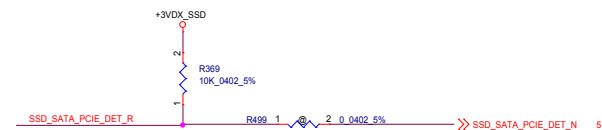
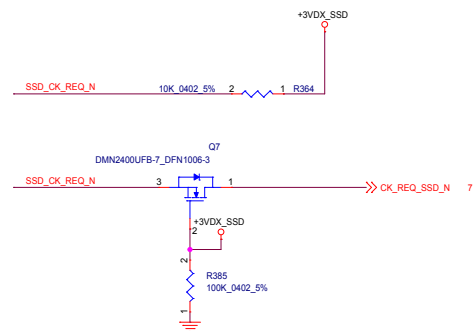
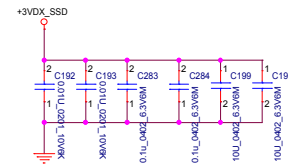
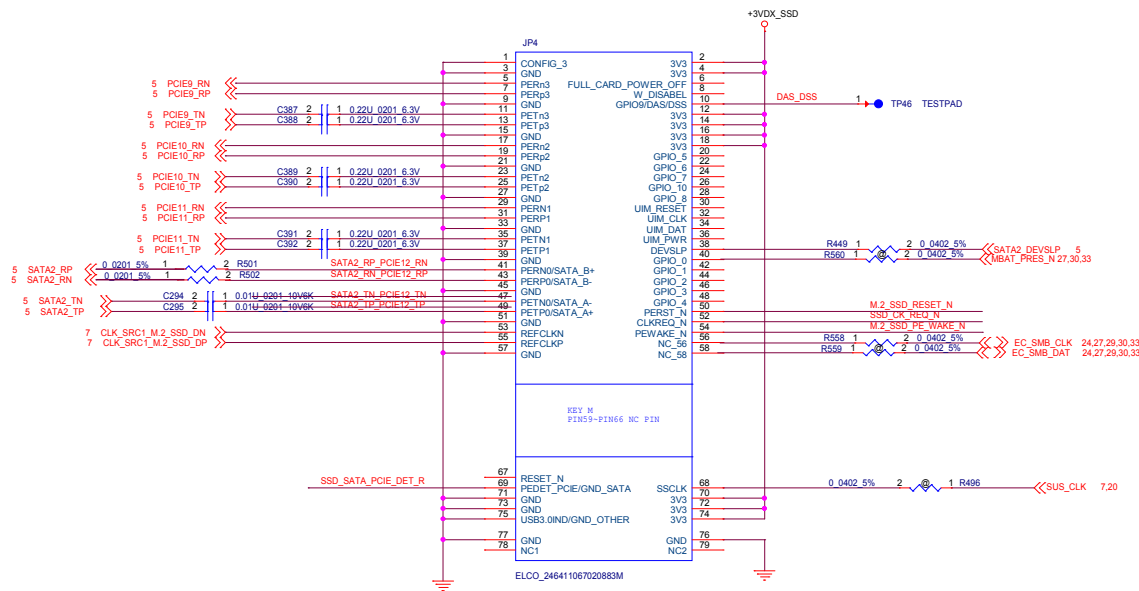
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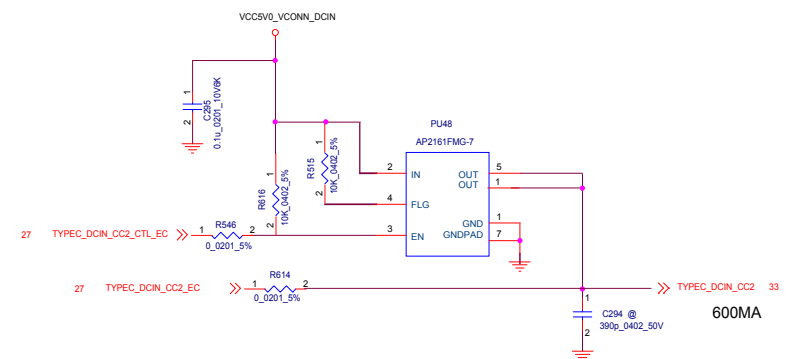
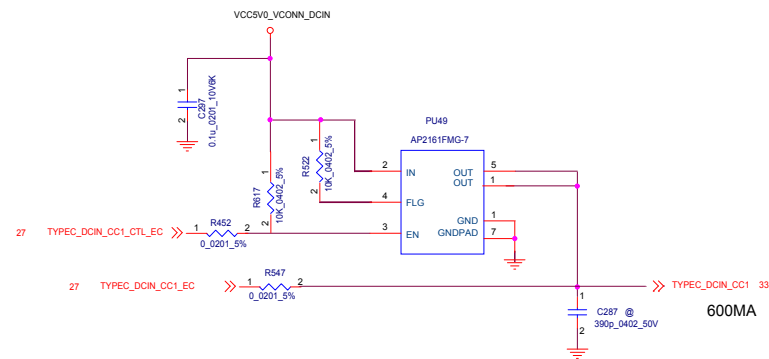
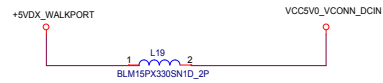
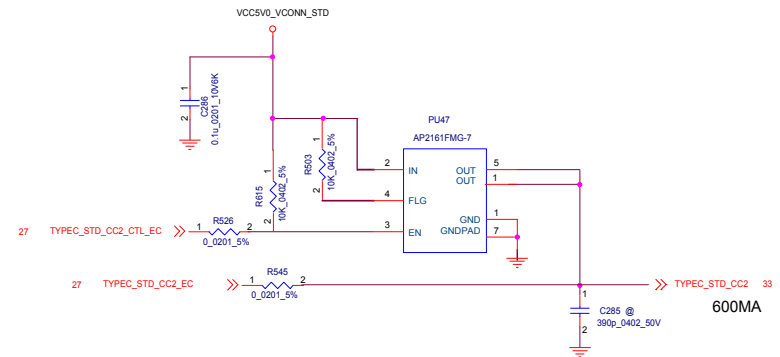
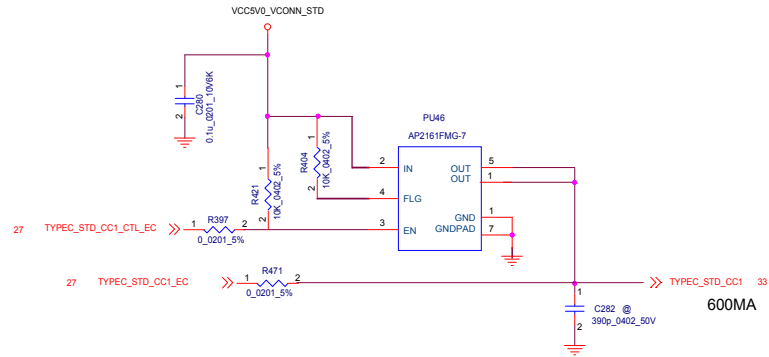
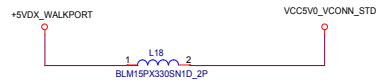


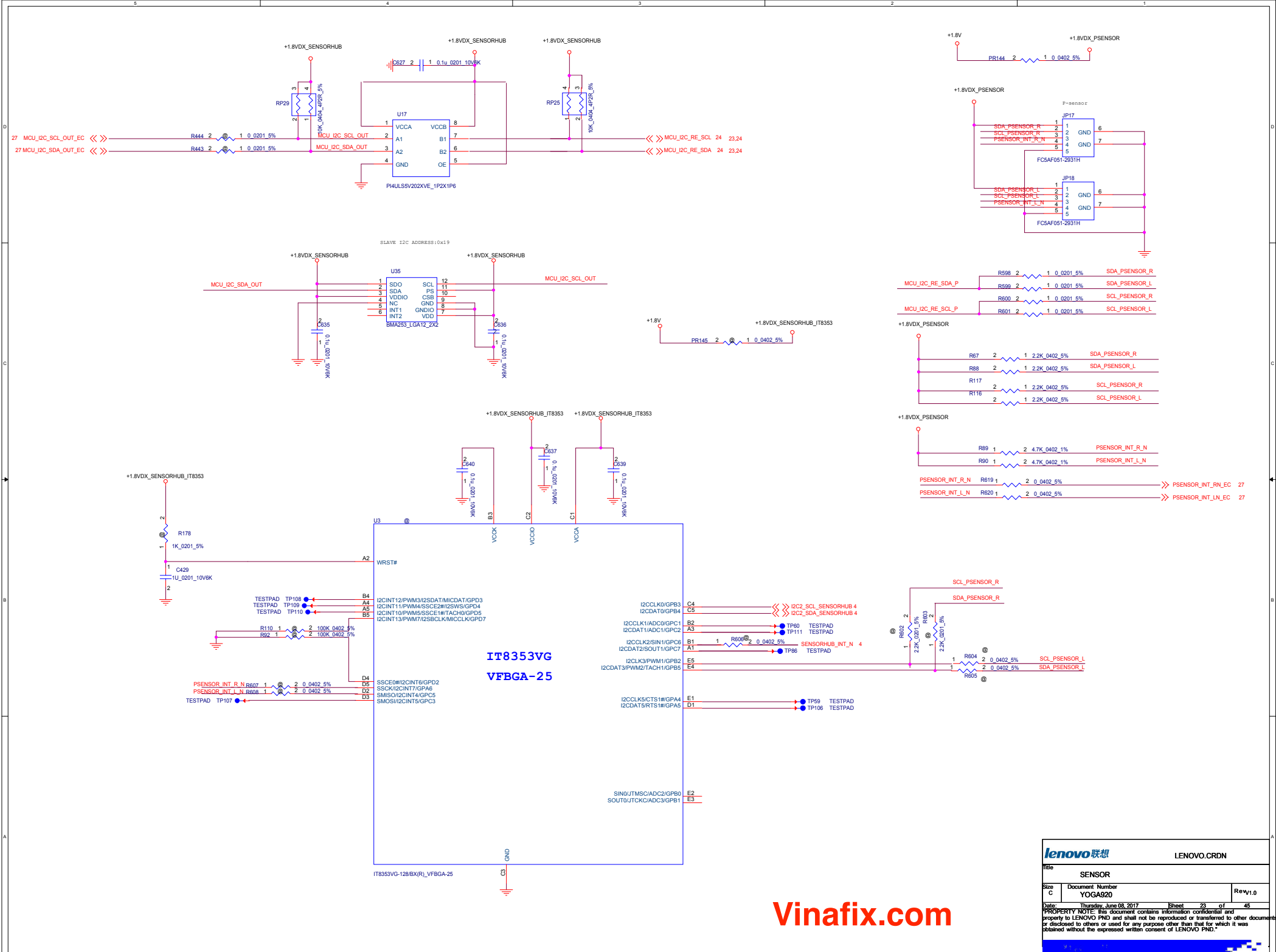


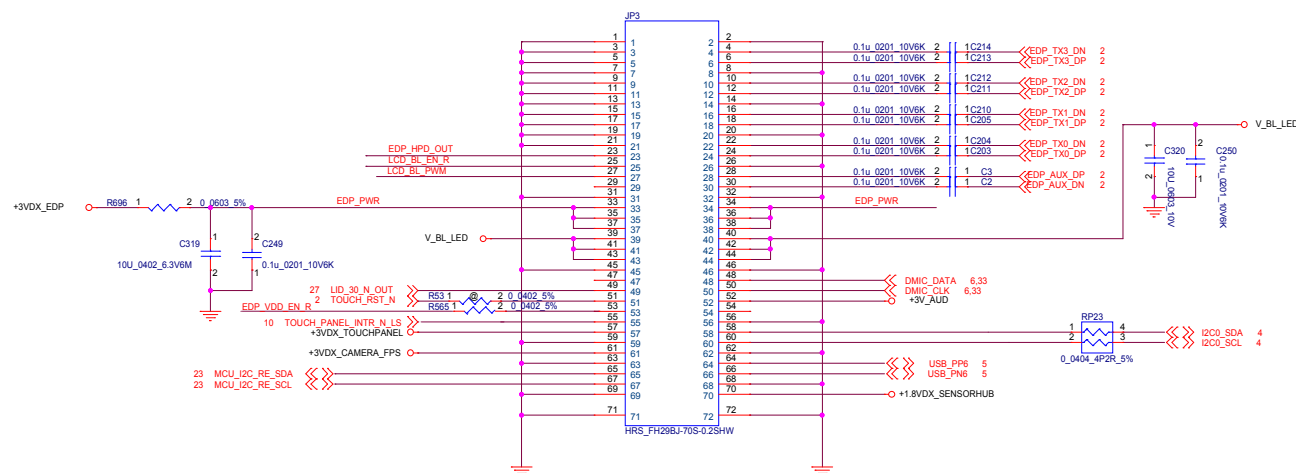
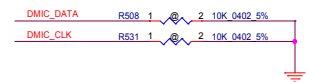
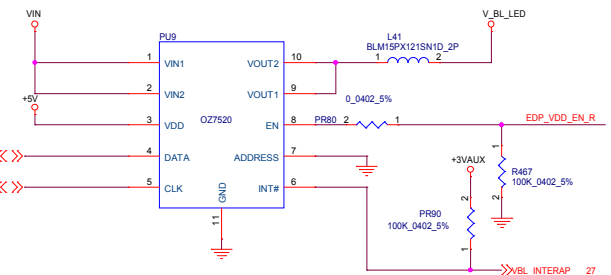
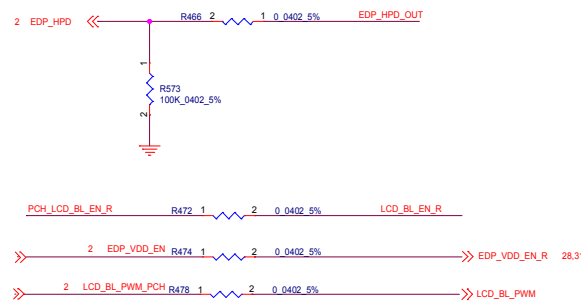




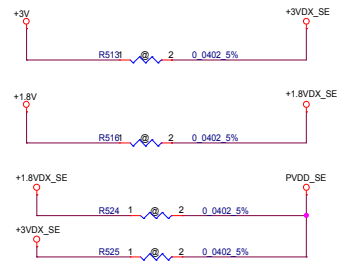
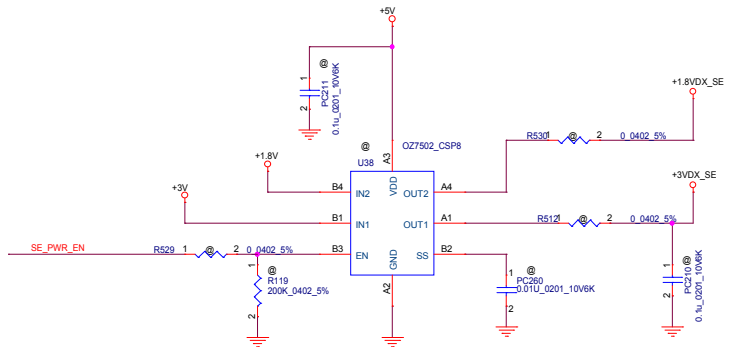
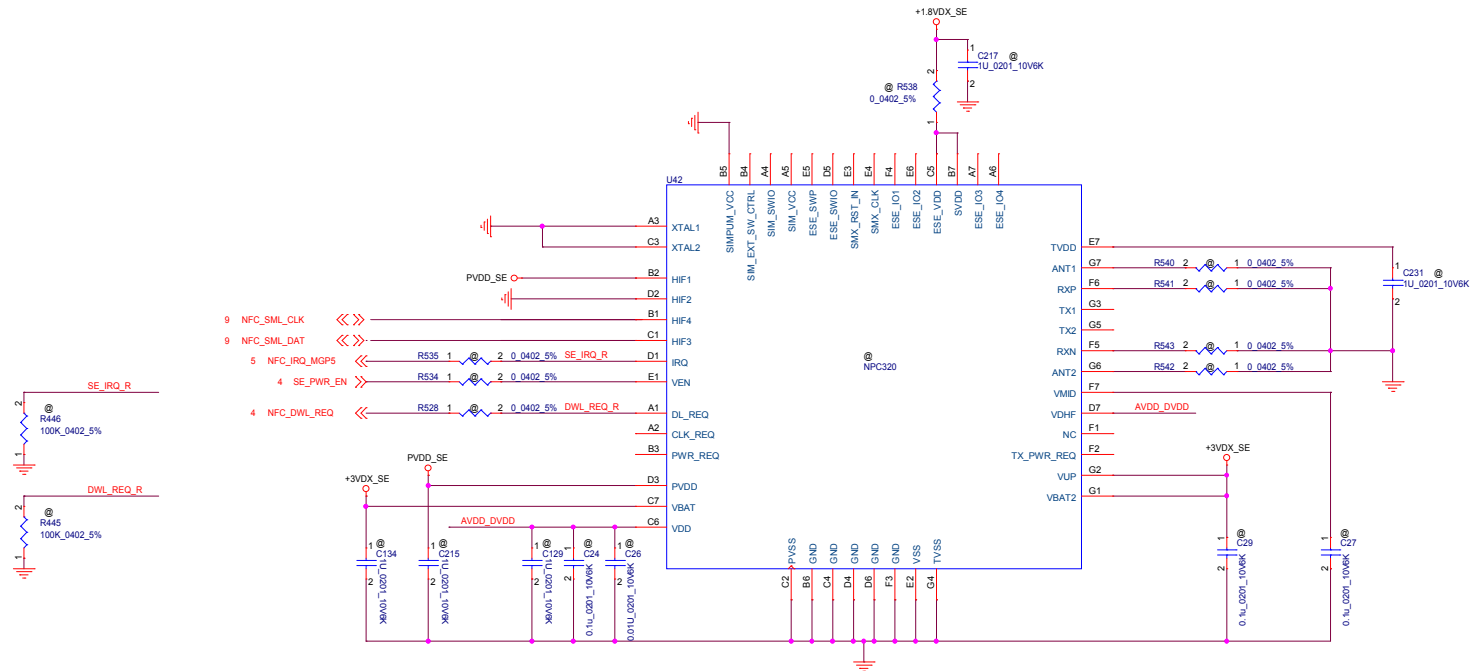


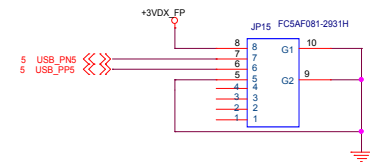
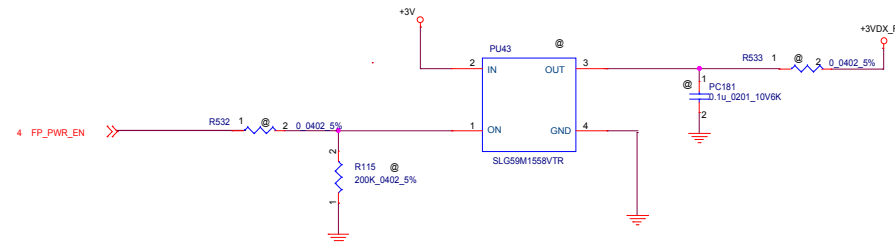
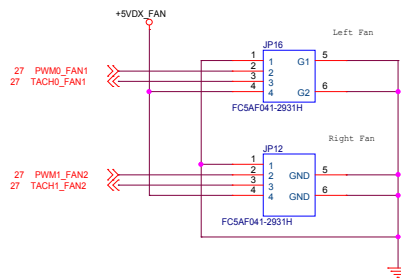
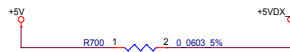
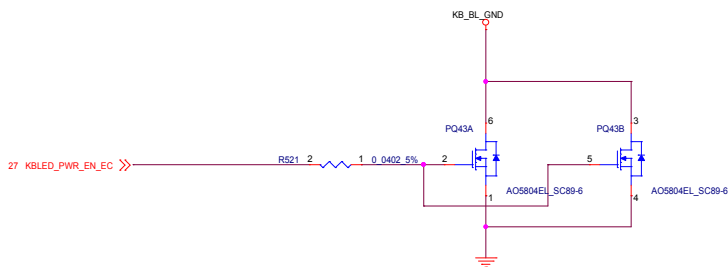
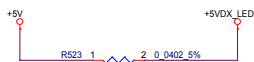
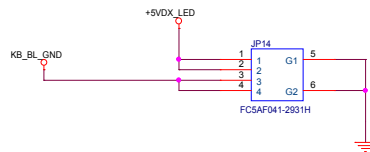
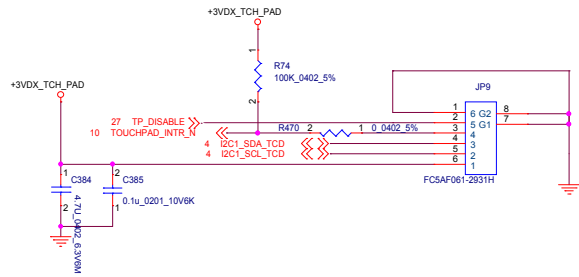


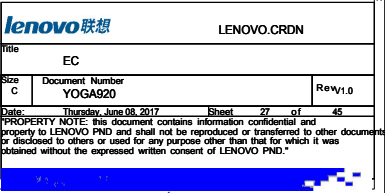


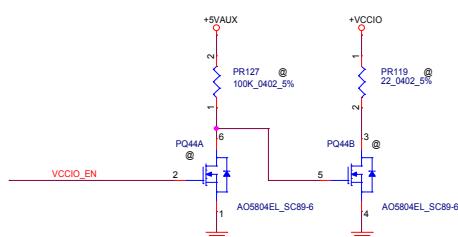
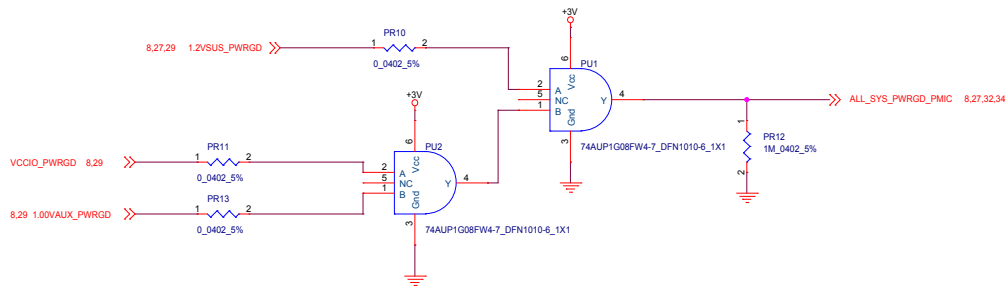
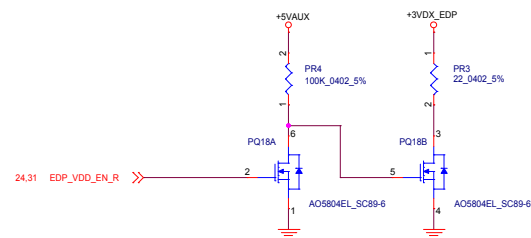
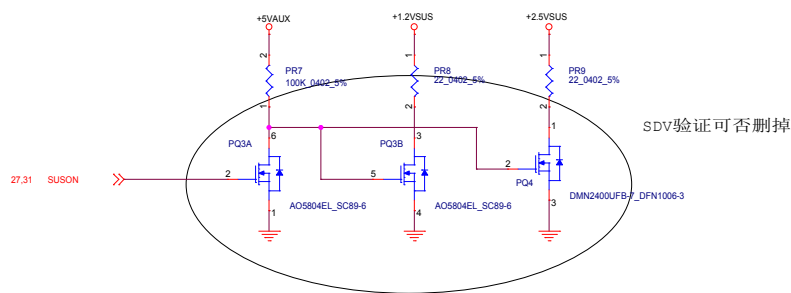
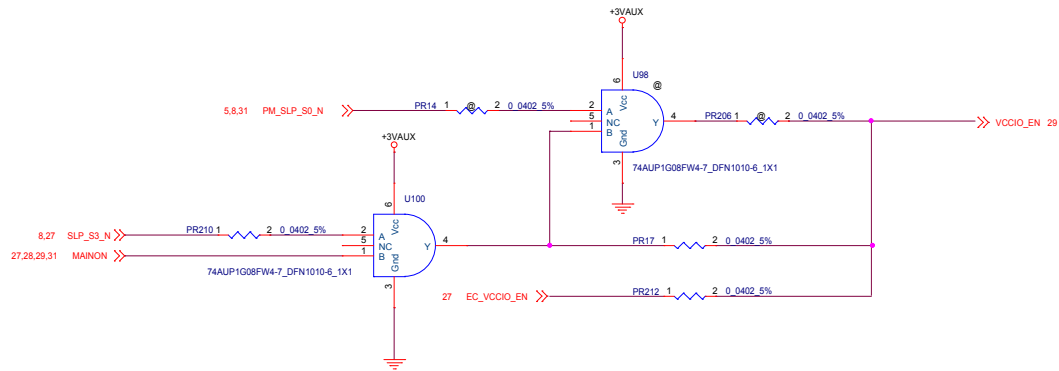
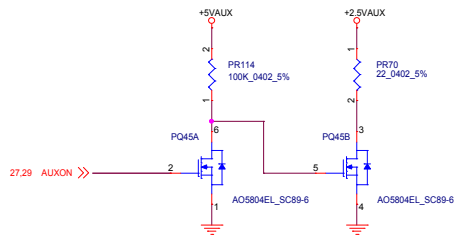


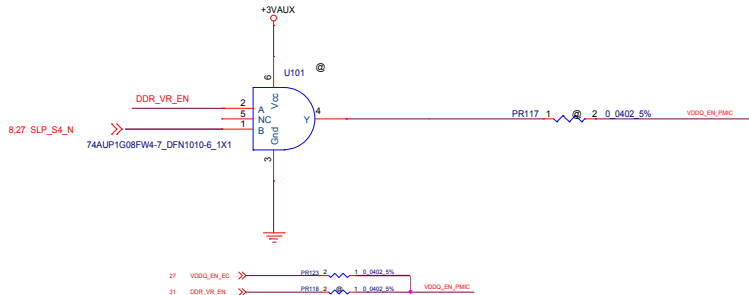
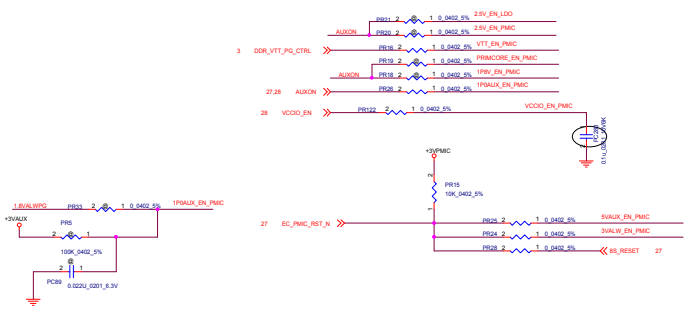
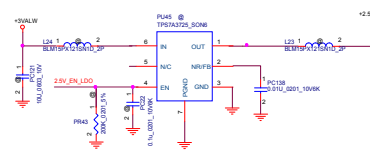
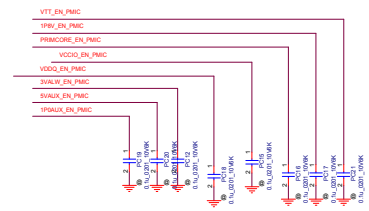
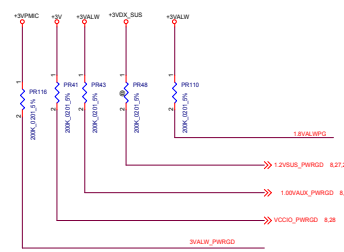
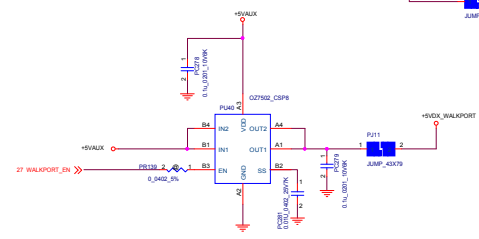
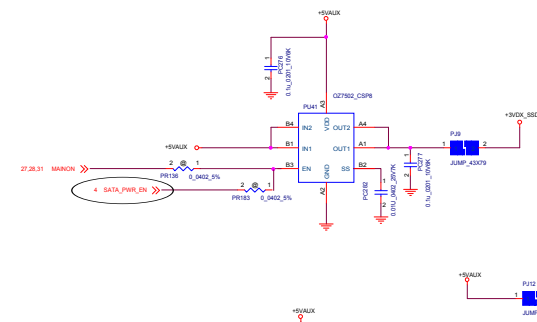






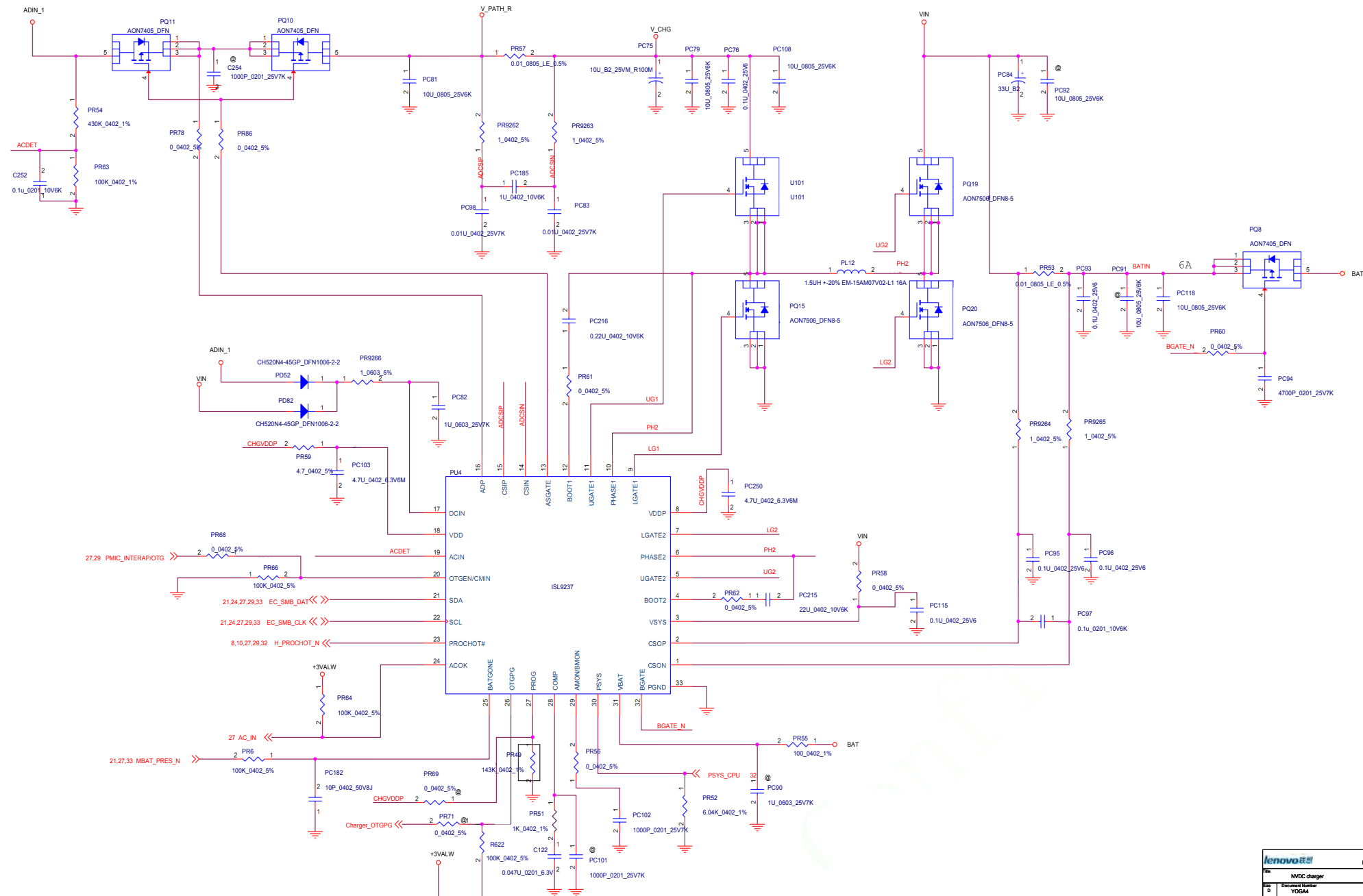


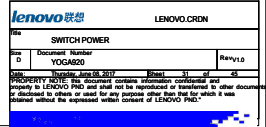




place close to J8 and J9

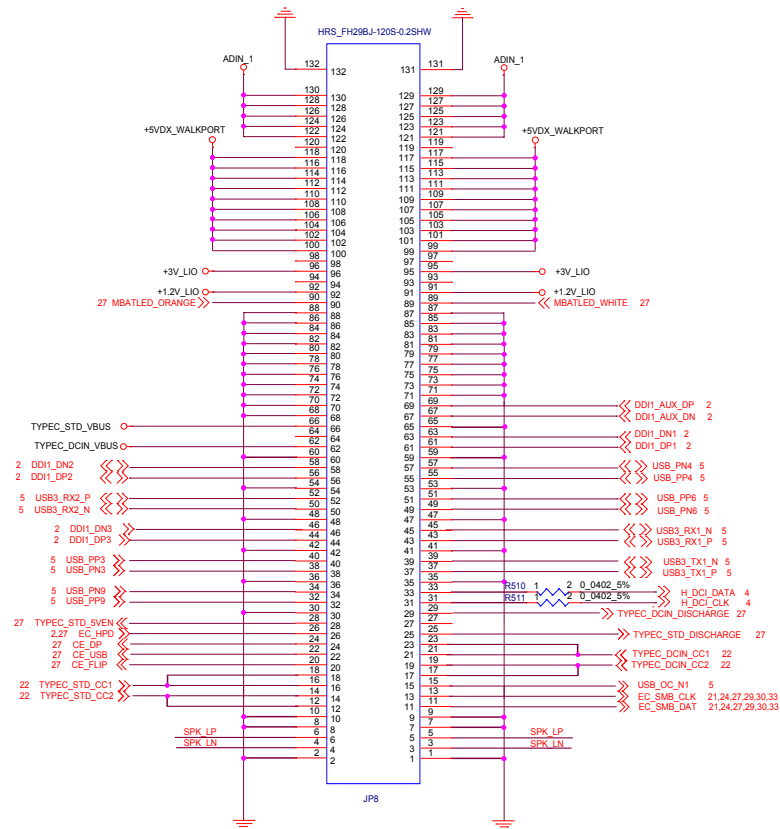
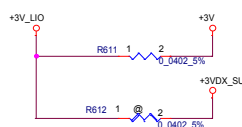
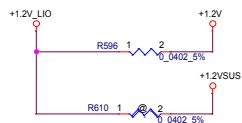
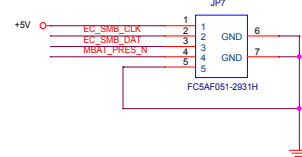
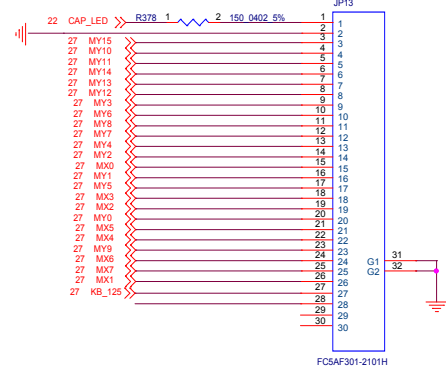
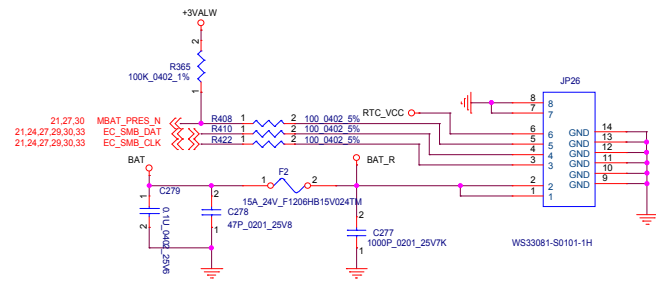
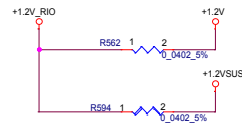
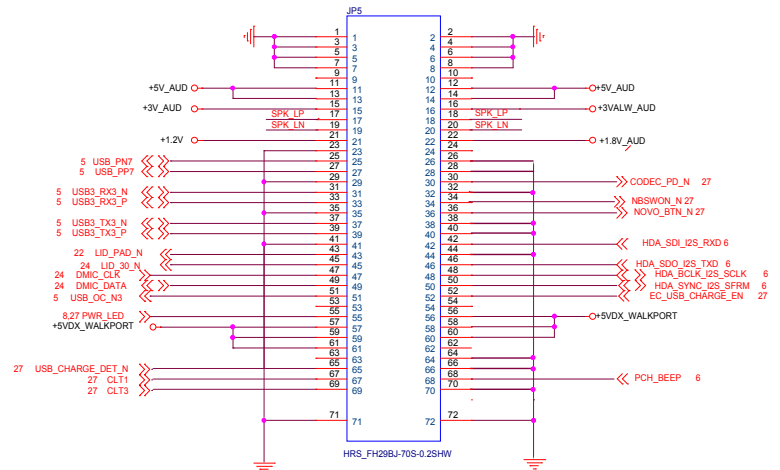
The diagram shows two capacitors, PC1 and PC2, connected to ground. PC1 is connected to a terminal labeled '1' and PC2 is connected to a terminal labeled '2'. Both capacitors are labeled '100nF'. A 5V input terminal labeled 'VIN' is also shown, connected to a terminal labeled '3'. The capacitors are placed close to J8 and J9.

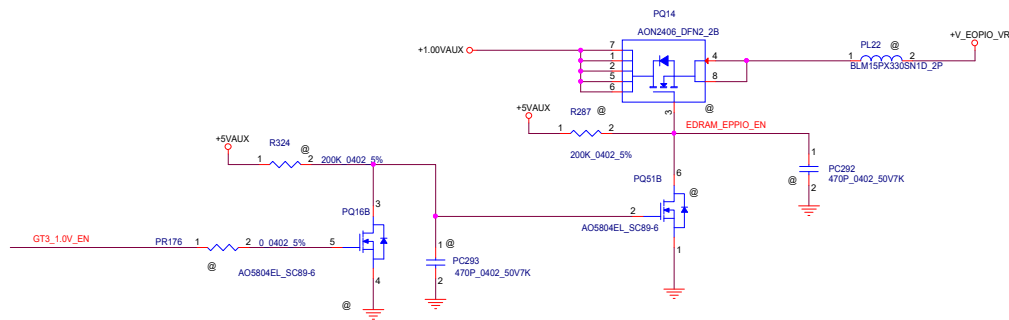
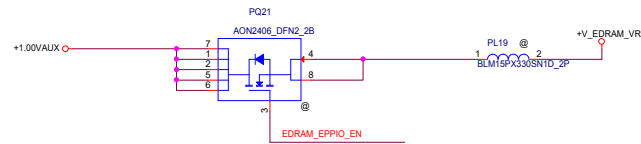
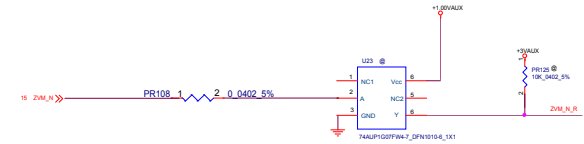
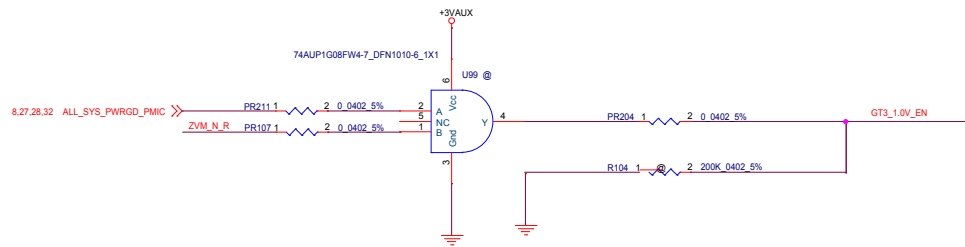




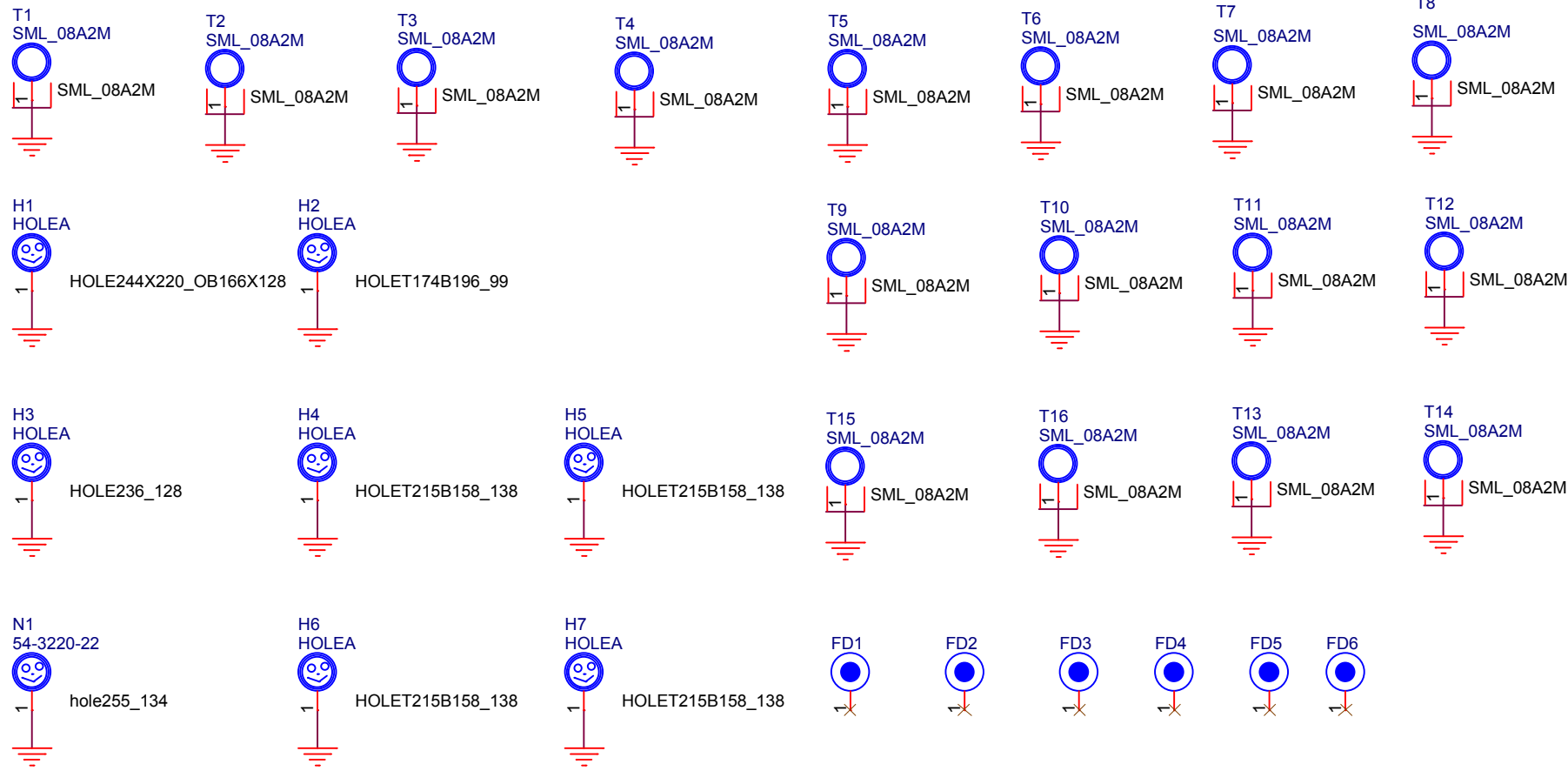













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